

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
40V	2.7mΩ@10V	110A
	3.2mΩ@4.5V	

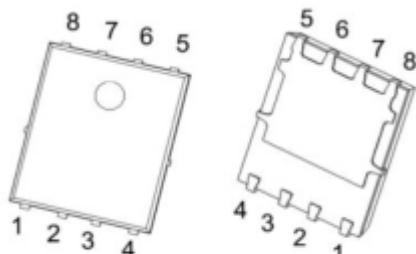
Feature

- Fast Switching
- Low Gate Charge and Rdson
- 100% Single Pulse avalanche energy Test

Application

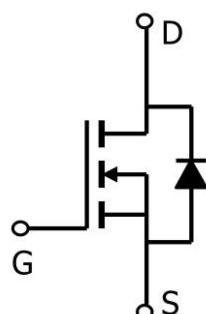
- Load Switch
- PWM Application
- Power Management

Package



PDFNWB5X6-8L

Circuit diagram

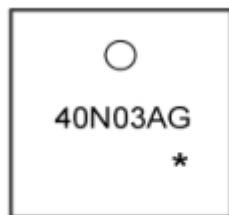




ZL MOSFET

ZL40N03AG

Marking



40N03AG =Device Code
* =Month Code

Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹ ($T_c=25^\circ\text{C}$)	I_D	110	A
Pulsed Drain Current ²	I_{DM}	440	A
Single Pulse Avalanche Energy ³	E_{AS}	462	mJ
Total Power Dissipation ⁴ ($T_c=25^\circ\text{C}$)	P_D	75	W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	1.67	$^\circ\text{C}/\text{W}$
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ\text{C}$

Electrical characteristics

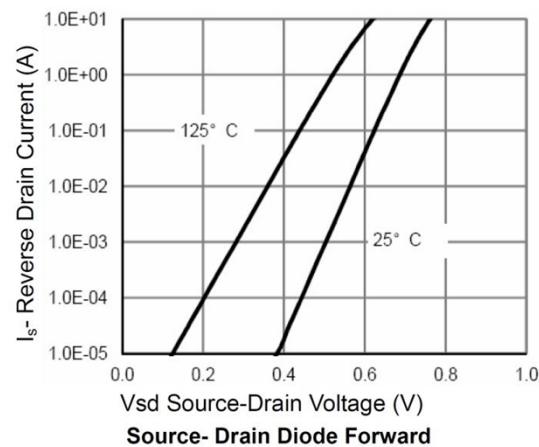
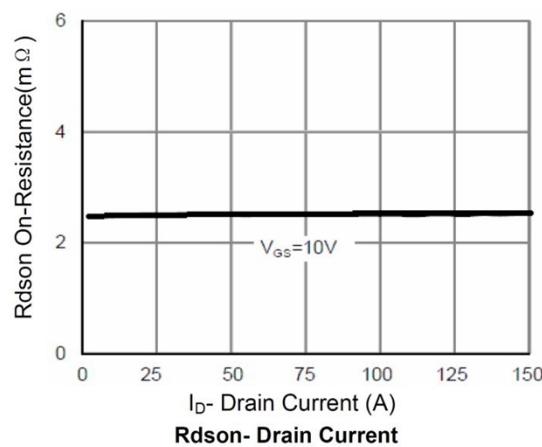
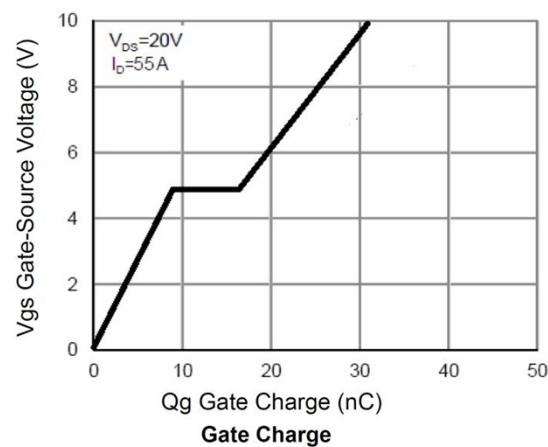
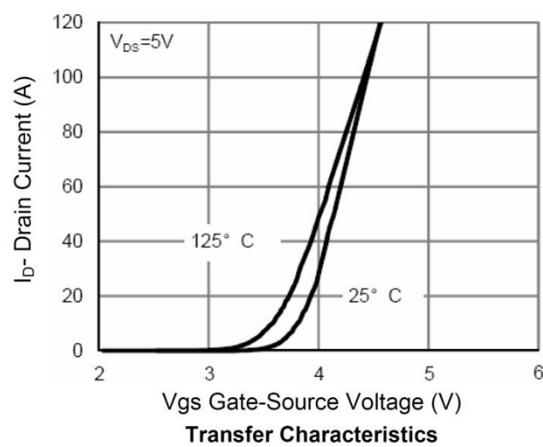
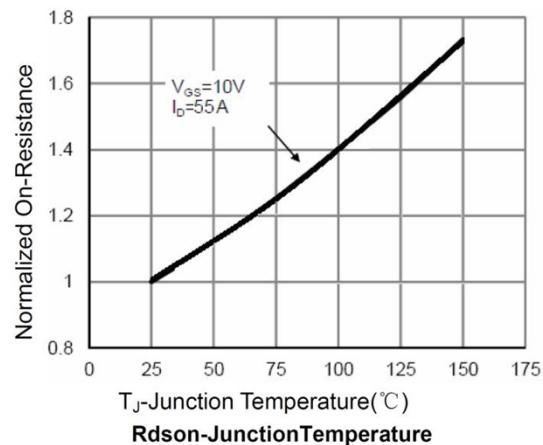
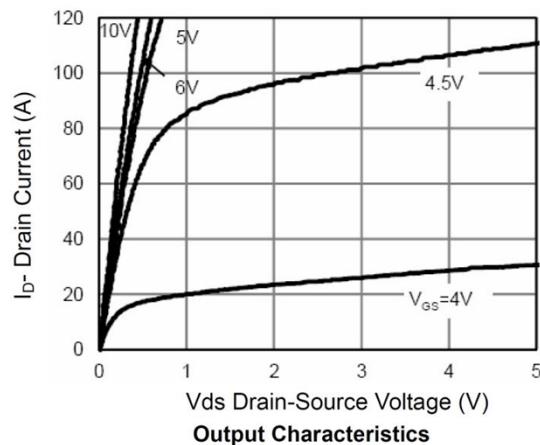
($T_A=25^\circ\text{C}$, unless otherwise noted)

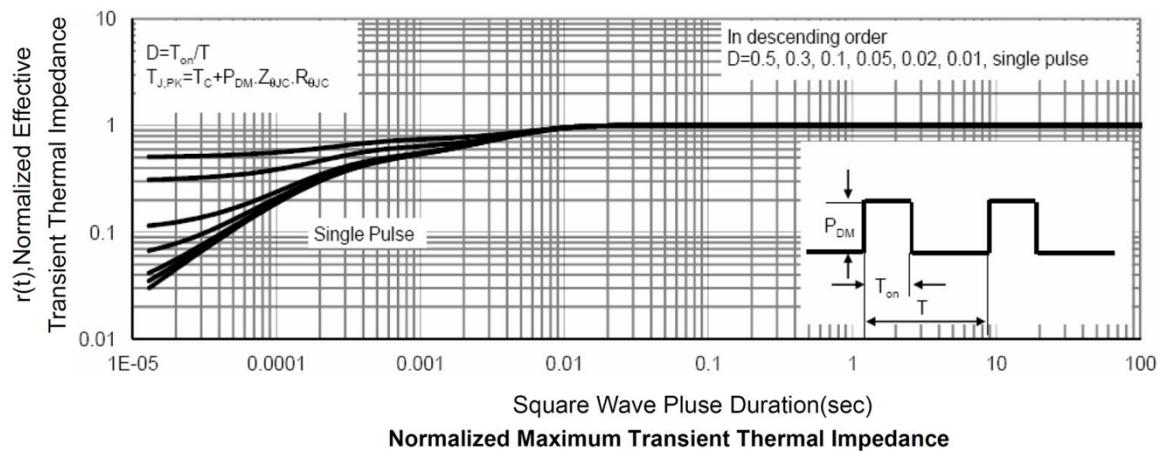
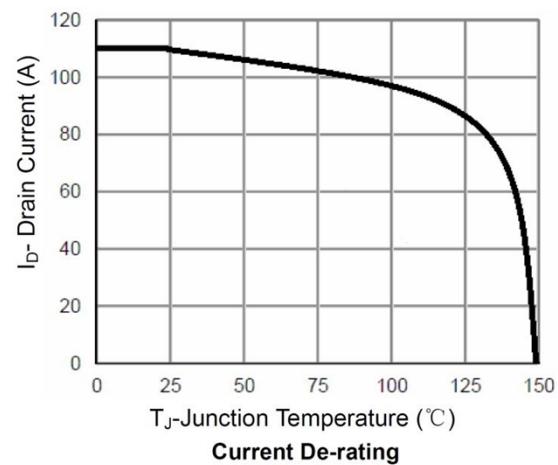
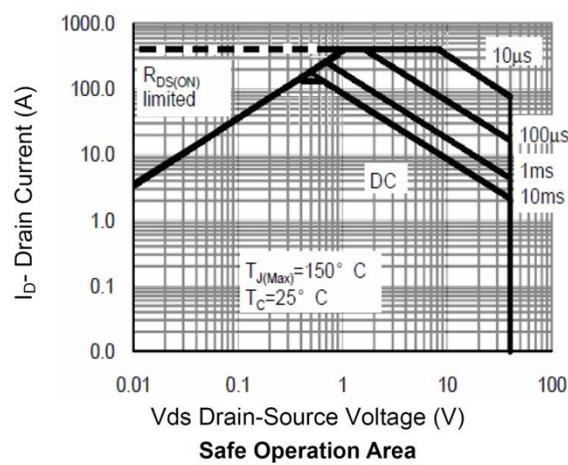
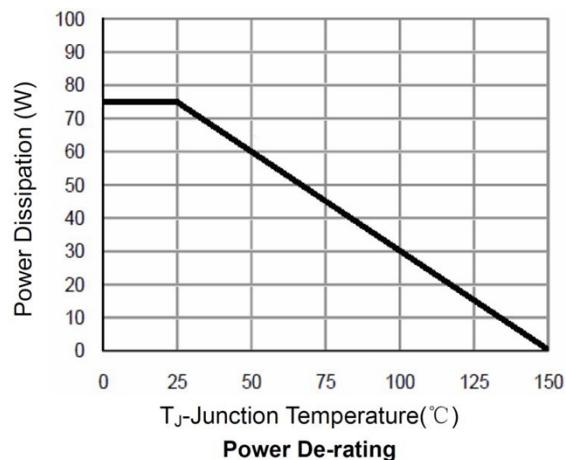
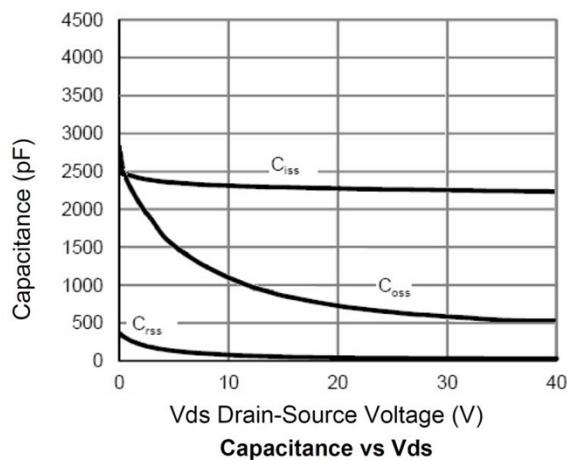
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$			± 100	μA
Gate-source threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.0	1.5	2.5	V
Static Drain-Source On-Resistance ²	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 30\text{A}$		2.7	3.5	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 30\text{A}$		3.2	4.3	
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		3210		pF
Output Capacitance	C_{oss}			750		
Reverse Transfer Capacitance	C_{rss}			52		
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = 20\text{V}, V_{GS} = 10\text{V}, I_D = 55\text{A}$		60		pF
Gate-Source Charge	Q_{gs}			10		
Gate-Drain Charge	Q_{gd}			9.5		
Turn-On Delay Time	$T_{d(on)}$	$V_{DD} = 20\text{V}, V_{GS} = 10\text{V}, R_G = 1.6\Omega, I_D = 55\text{A}$		11		nS
Rise Time	T_r			5		
Turn-Off Delay Time	$T_{d(off)}$			32		
Fall Time	T_f			5.5		
Diode Characteristics						
Diode Forward Voltage ²	V_{SD}	$V_{GS} = 0\text{V}, I_S = 1\text{A}, T_J = 25^\circ\text{C}$			1.2	V

Note:

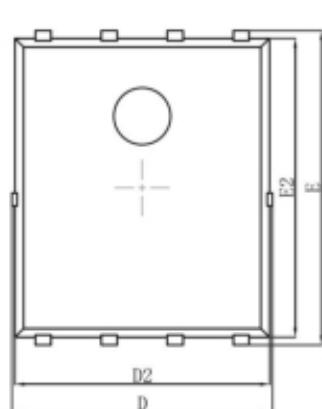
1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is $V_{DD} = 20\text{V}, V_{GS} = 10\text{V}, L = 0.5\text{mH}, R_G = 25\Omega$

Typical Characteristics

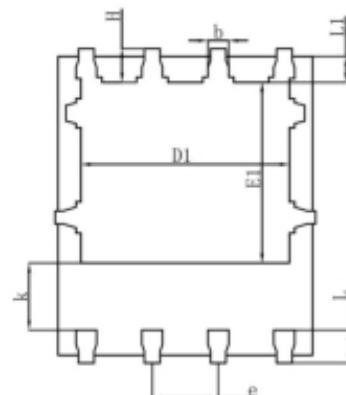




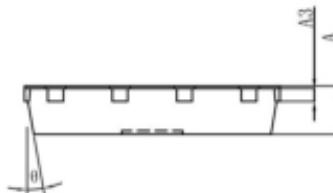
PDFNWB5X6-8L Package Information



Top View
[顶视图]



Bottom View
[底视图]



Side View
[侧视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.			0.010REF.
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°