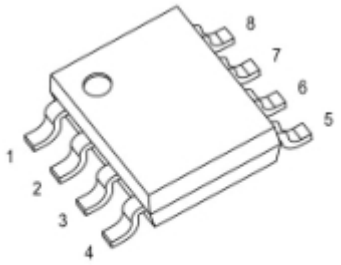
Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
-100V	230mΩ@-10V	-3.5A
	240mΩ@-4.5V	

Feature

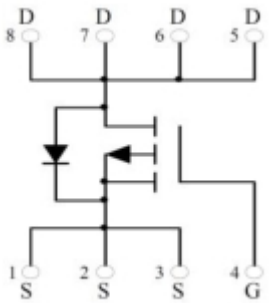
- High density cell design for ultra low RDS(on)
- Fast Switching Speed
- Low Gate Charge

Package

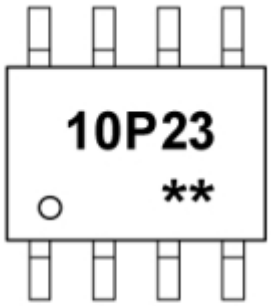


SOP-8L

Circuit diagram



Marking



10P23 =Device Code
** =Week Code

Absolute maximum ratings

(T_a=25℃ unless otherwise noted)

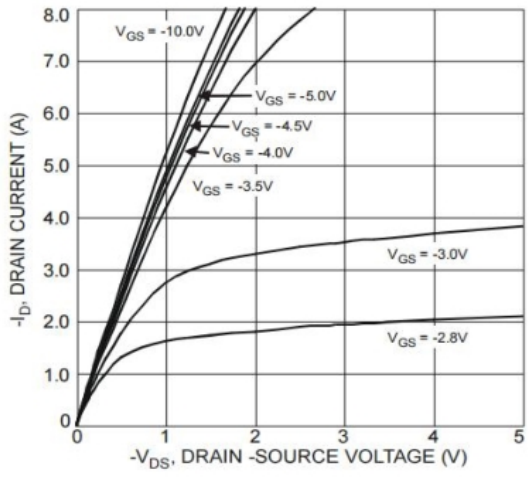
Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current 0(T_A = 25℃)	I_D	-3.5	W
Pulsed Drain Current[1]	I_{DM}	-14	A
Power Dissipation (T_A = 25℃)	P_D	3	W
Thermal Resistance Junction-ambient	$R_{\theta JA}$	41.7	℃/ W
Storage Temperature	T_{STG}	-55~ +150	℃
Junction Temperature	$T_{J,}$	-55~ +150	℃

Electrical characteristics

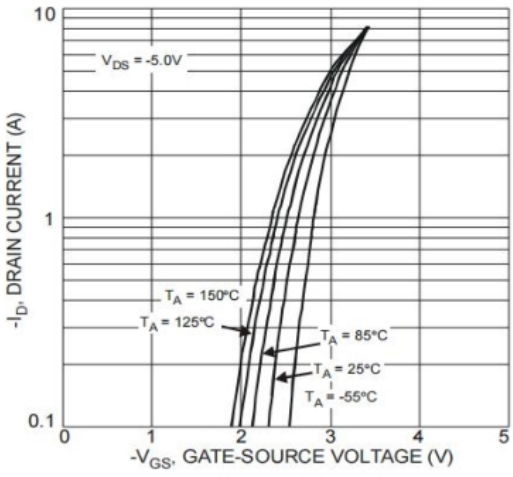
(T_A=25℃, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS} = 0V, I_D =-250μA	-100			V
Drain-Source Leakage Current	I_{DSS}	V_{DS} =-800V,V_{GS} = 0V			-1	uA
Gate-body leakage current	I_{GSS}	V_{GS} =±20V , V_{DS}=0V			±100	uA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =-250μA	-1	-1.5	-2.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} =-10V, I_D =-2A		230	290	mΩ
		V_{GS} =-4.5V, I_D =-1A		240	320	
Dynamic characteristics[4]						
Input Capacitance	C_{iss}	V_{DS}=-25V,V_{GS}=0V, f=1MHz		1239		pF
Output Capacitance	C_{oss}			42		
Reverse Transfer Capacitance	C_{rss}			38		
Switching Characteristics						
Turn-On Delay Time	$T_{d(on)}$	V_{DD}=-50V, V_{GS}=-10V, R_G=10Ω, I_D=-2A		9.1		nS
Rise Time	T_r			14.9		
Turn-Off Delay Time	$T_{d(off)}$			57.4		
Fall Time	T_f			34.4		
Total Gate Charge(4.5V)	Q_g	V_{DS}=-60V, V_{GS}=-10V, I_D=-2A		17.5		nC
Gate-Source Charge	Q_{gS}			2.8		
Gate-Drain Charge	Q_{gd}			3.2		
Source-Drain Diode Characteristics						
Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_S=-1A			-1.2	V

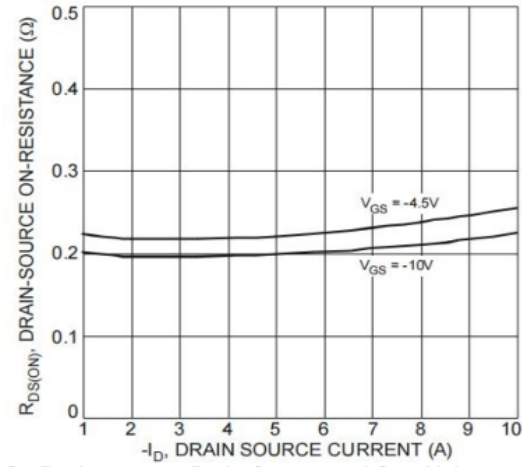
Typical Characteristics



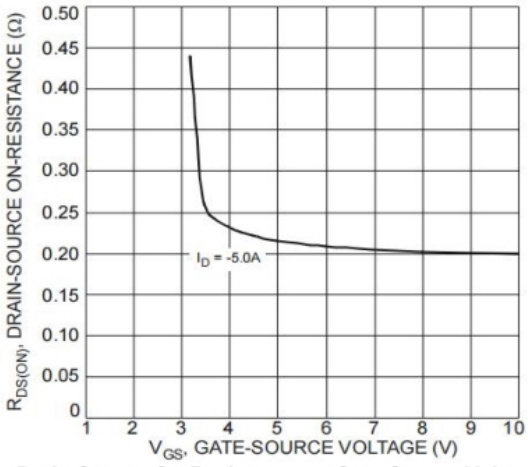
Typical Output Characteristics



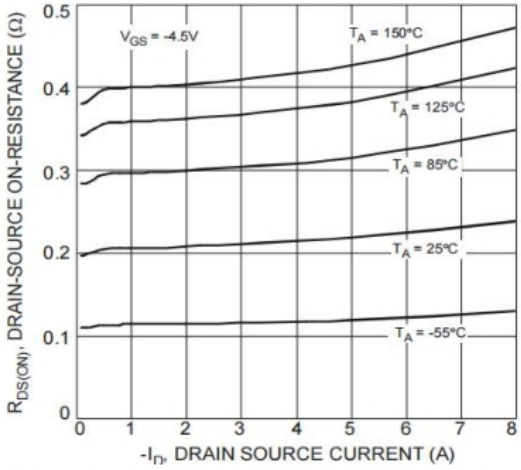
Transfer Characteristics



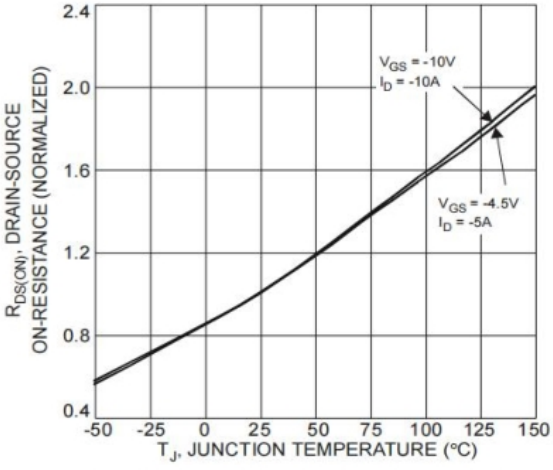
On-Resistance vs. Drain Current and Gate Voltage



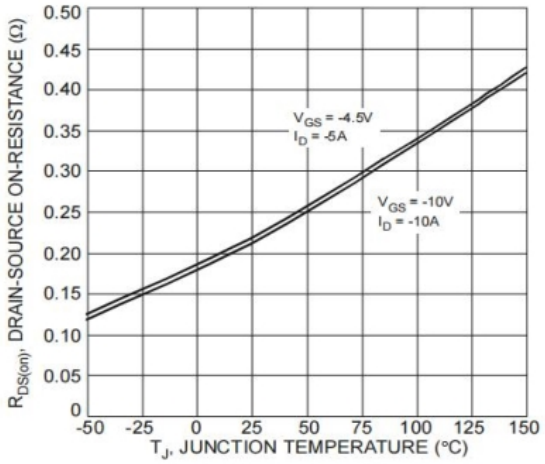
Drain-Source On-Resistance vs.Gate-Source Voltage



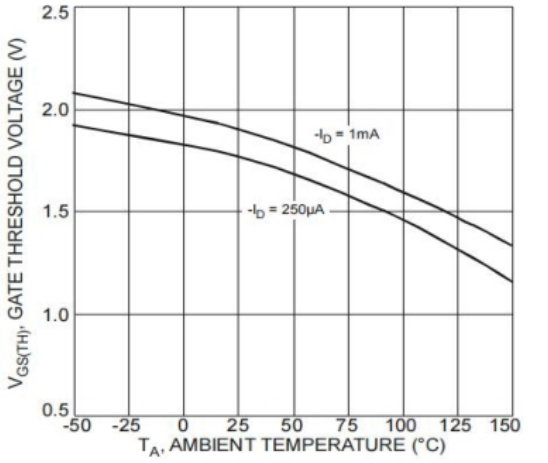
On-Resistance vs. Drain Current and Temperature



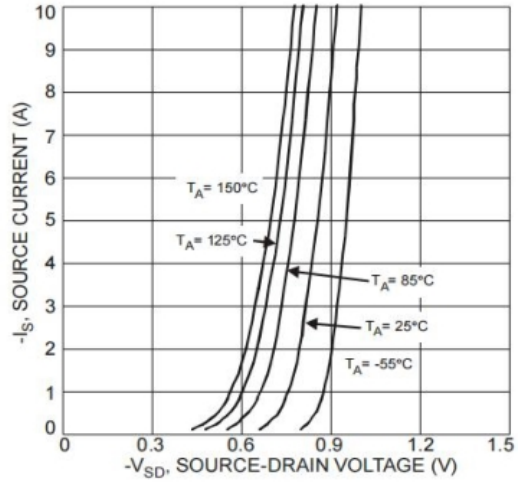
On-Resistance Variation with Temperature



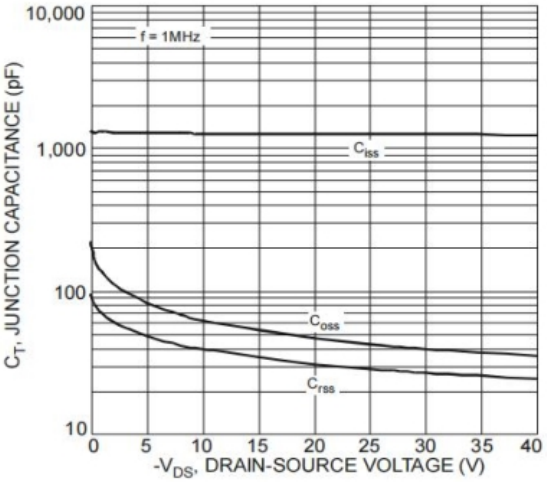
On-Resistance Variation with Temperature



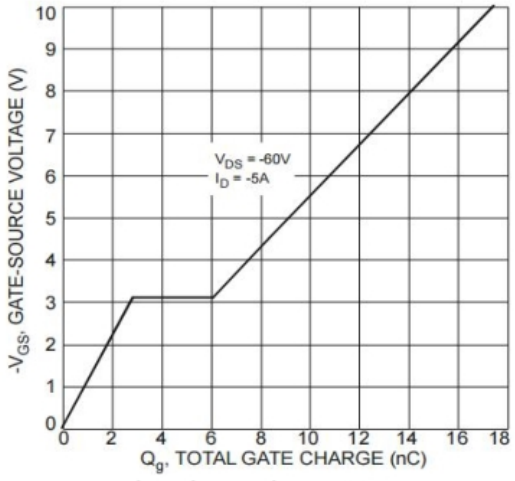
Gate Threshold Variation vs. Ambient Temperature



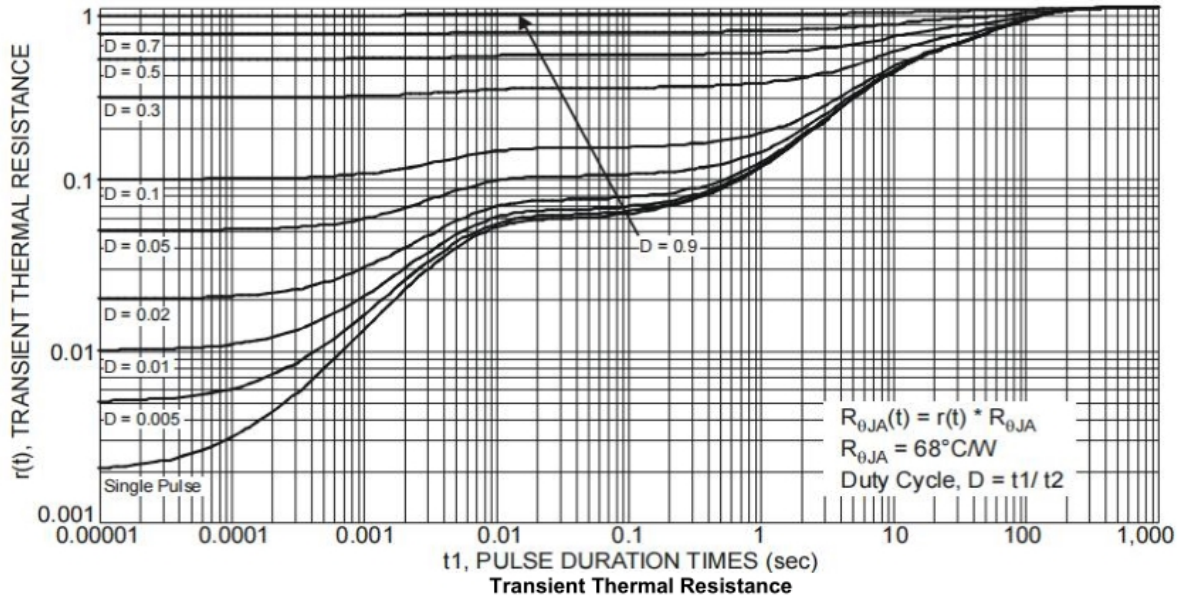
Diode Forward Voltage vs. Current



Typical Junction Capacitance

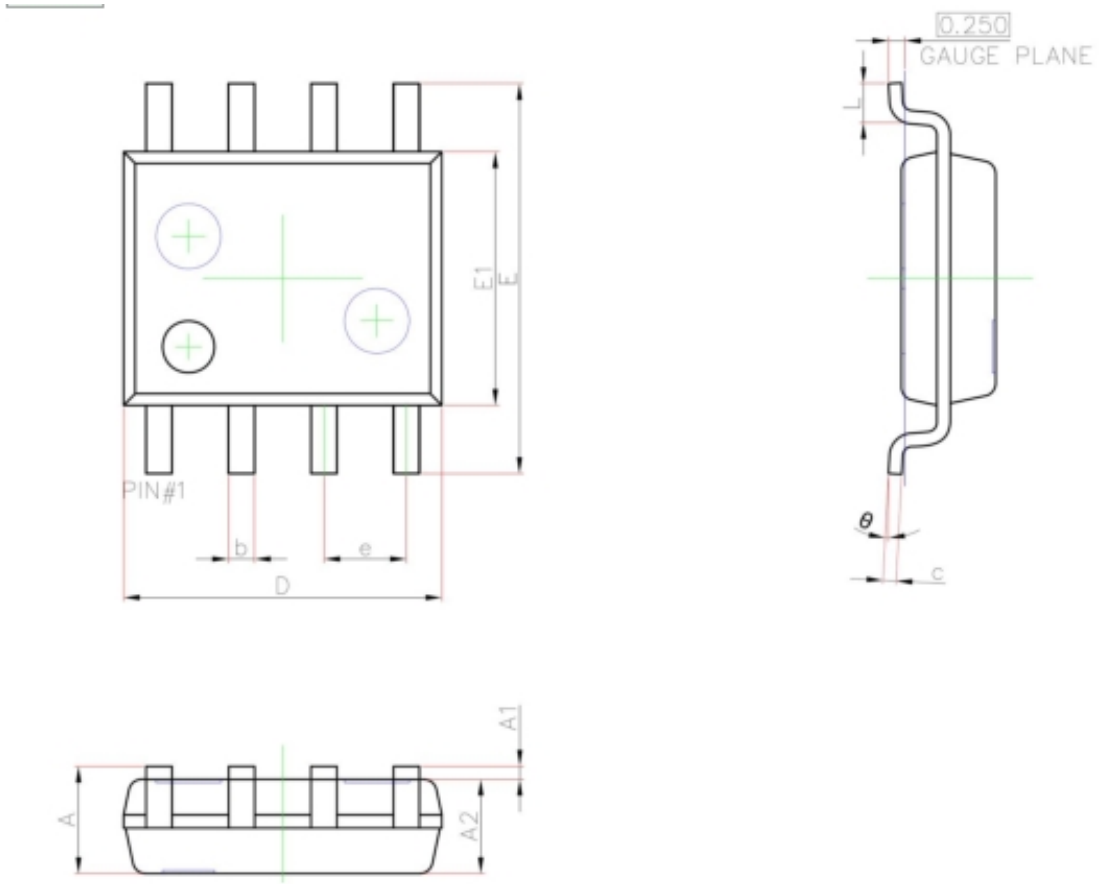


Gate-Charge Characteristics



Transient Thermal Resistance

SOP-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.450	1.750	0.057	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°