

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
100V	18mΩ@10V	60A

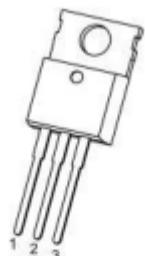
Feature

- Fast Switching
- Low Gate Charge and Rdson
- 100% Single Pulse avalanche energy Test

Application

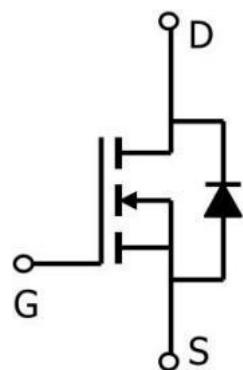
- DC-DC Converter
- Ideal for high-frequency switching and synchronous rectification

Package

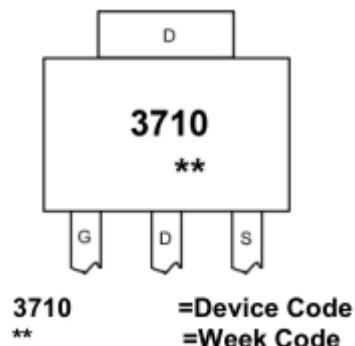


TO-220-3L-C(G:1 D:2 S:3)

Circuit diagram



Marking



Absolute maximum ratings

($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹ ($T_c=25^\circ\text{C}$)	I_D	60	A
Pulsed Drain Current ²	I_{DM}	240	A
Single Pulse Avalanche Energy ³	E_{AS}	1060	mJ
Total Power Dissipation($T_c=25^\circ\text{C}$)	P_D	200	W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	0.625	$^\circ\text{C}/\text{W}$
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ\text{C}$

Electrical characteristics

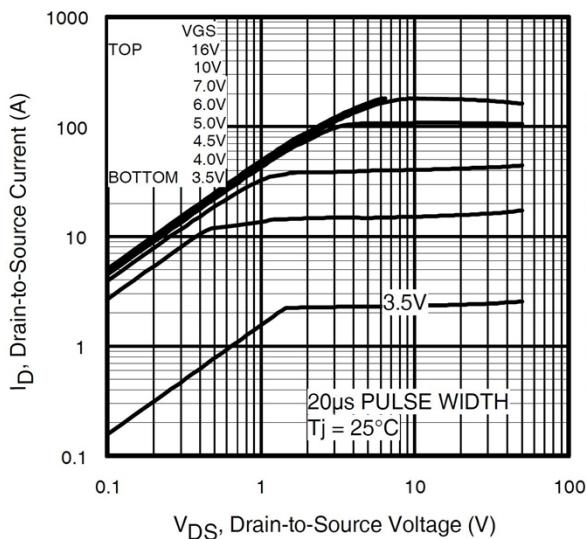
($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$\text{BV}_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Bvdss Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_D=250\mu\text{A}$, Reference 25°C		0.13		$\text{V}/{}^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$			25	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 25\text{V}, V_{DS} = 0\text{V}$			± 100	μA
Gate threshold voltage ⁽¹⁾	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3	4	V
Static Drain-Source on-Resistance	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$		18	23	$\text{m}\Omega$
Dynamic Characteristics⁴⁾						
Input capacitance	C_{iss}	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$		3230		pF
Output capacitance	C_{oss}			430		
Reverse transfer capacitance	C_{rss}			69		
Switching Characteristics⁴⁾						
Total gate charge	Q_g	$V_{GS} = 50\text{V}, V_{DS} = 10\text{V}, I_D = 60\text{A}$		134		nC
Gate-source charge	Q_{gs}			22		
Gate-drain charge	Q_{gd}			48		
Turn-on Delay Time	$T_{d(on)}$	$V_{DD}=180\text{V}, V_{GS}=10\text{V}, R_G=10\Omega, I_D = 20\text{A}$		28		nS
Turn-on Rise Time	T_r			47		
Turn-Off Delay Time	$T_{d(off)}$			57		
Turn-Off Fall Time	t_f			40		

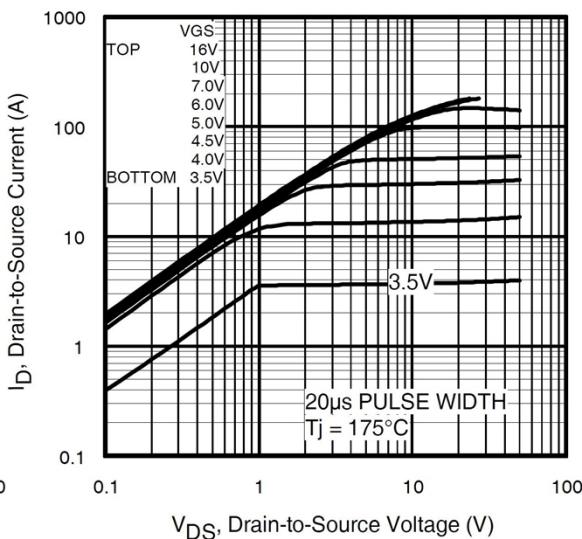
Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZcopper.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is $V_{DD}=50\text{V}, R_G = 25\Omega, L=0.5\text{mH}$

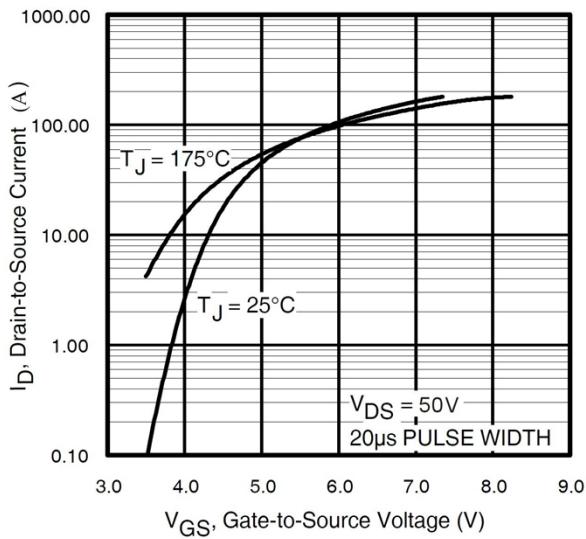
Typical Characteristics



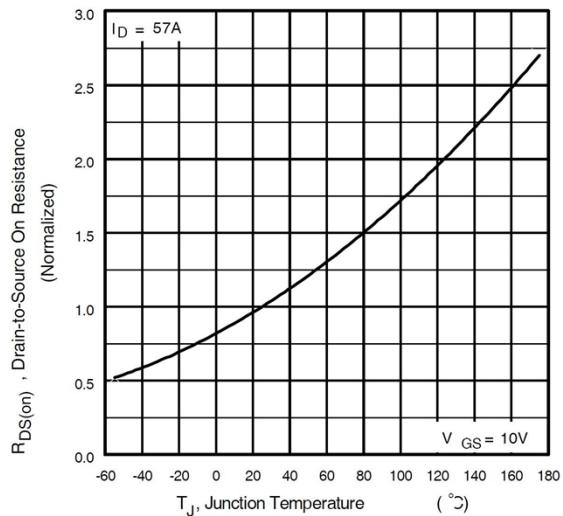
Typical Output Characteristics



Typical Output Characteristics



Typical Transfer Characteristics

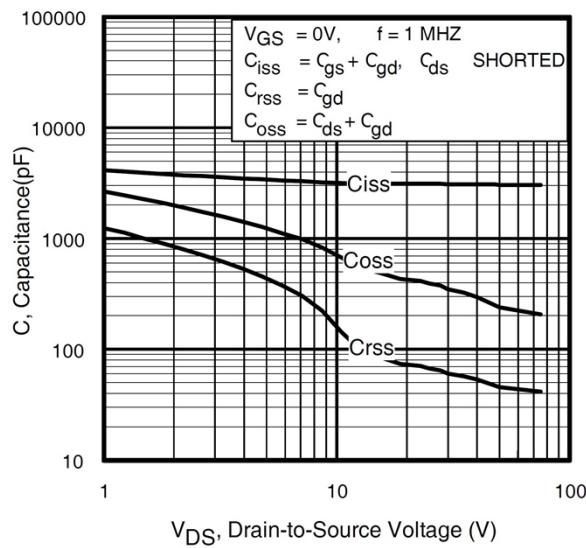


Normalized On-Resistance Vs. Temperature

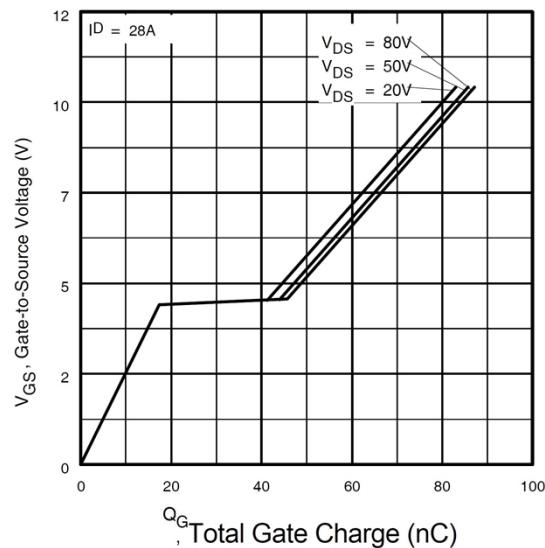


ZL MOSFET

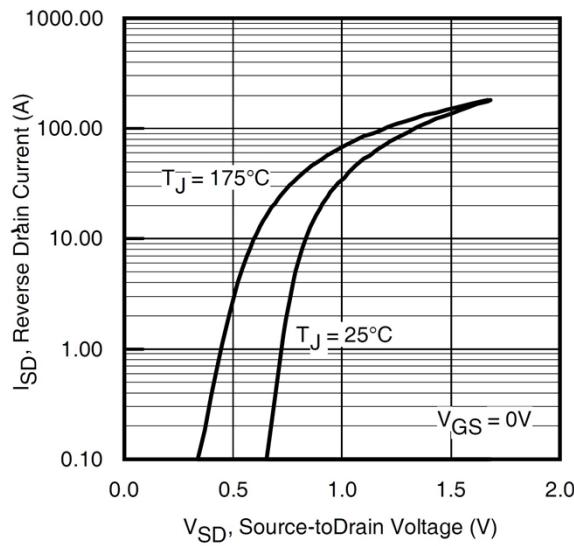
ZL3710



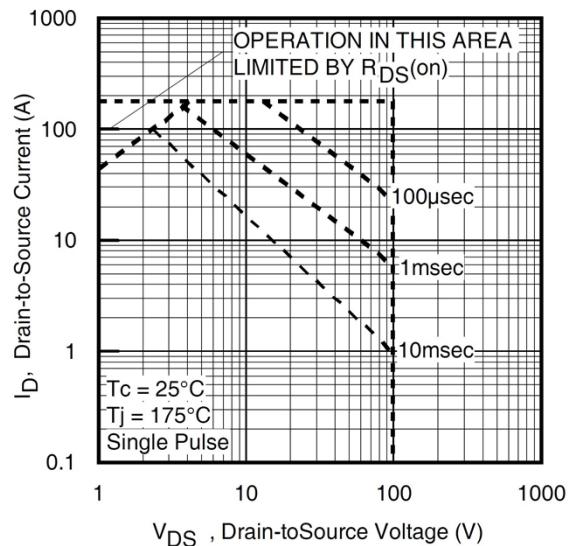
Typical Capacitance Vs.
Drain-to-Source Voltage



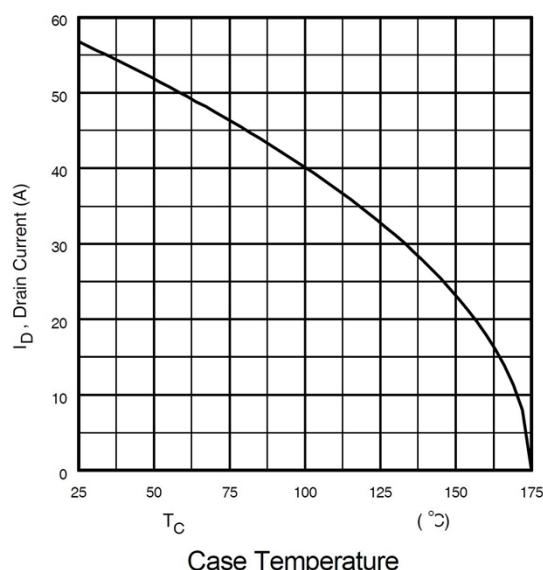
Typical Gate Charge Vs. Gate-
to-Source Voltage



Typical Source-Drain Diode
Forward Voltage



Maximum Safe Operating Area



**Maximum Drain Current Vs.
Case Temperature**

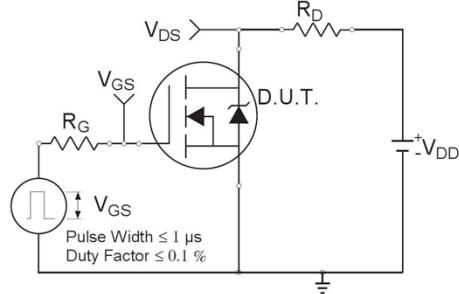
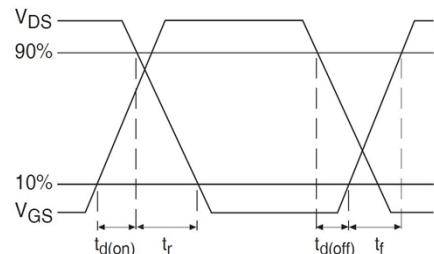
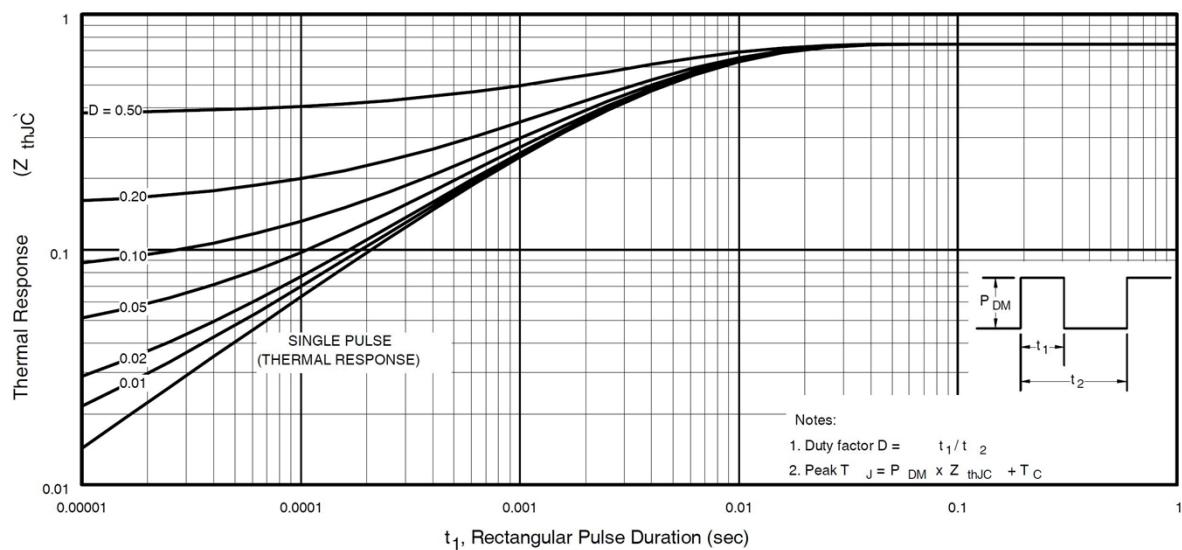


Fig 10a. Switching Time Test Circuit

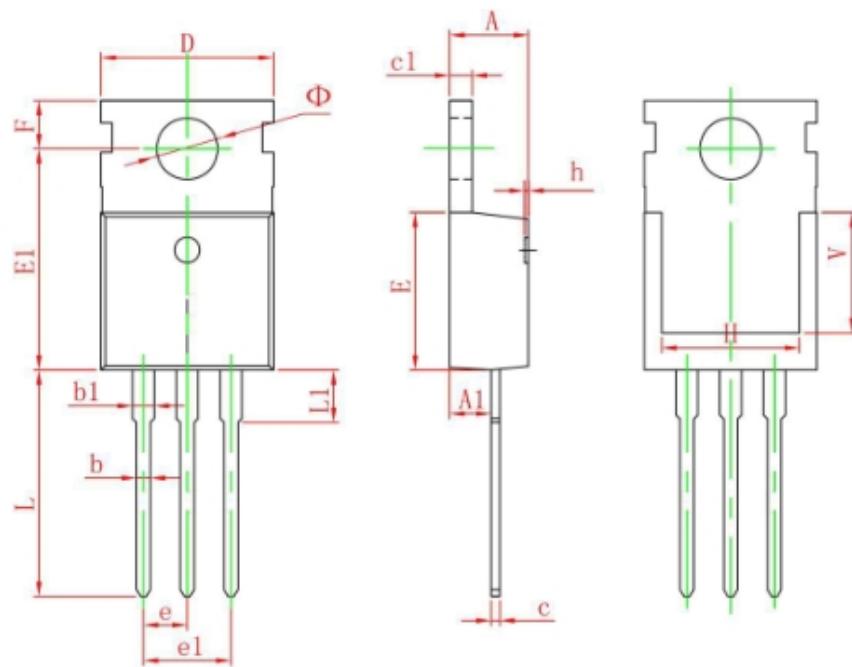


Switching Time Waveforms



Maximum Effective Transient Thermal Impedance, Junction-to-Case

TO-220-3L-C Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.950	9.750	0.352	0.384
E1	12.650	13.050	0.498	0.514
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	6.900 REF.		0.276 REF.	
Φ	3.400	3.800	0.134	0.150