

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
80V	2.9m Ω @10V	200A

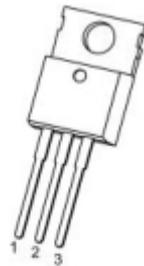
Feature

- Fast Switching
- Low Gate Charge and Rdson
- 100% Single Pulse avalanche energy Test

Applications

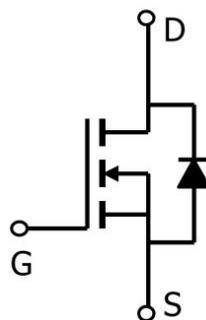
- Power switching application
- DC-DC Converter
- Power Management

Package

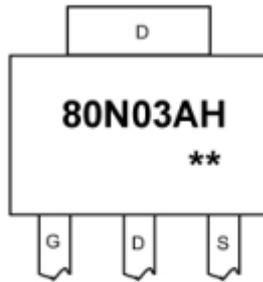


TO-220-3L(1:G 2:D 3:S)

Circuit diagram



Marking



80N03AH : Product code
 ** : Week code

Absolute maximum ratings

(T_a=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain source voltage	V _{DS}	80	V
Gate source voltage	V _{GS}	±20	V
Continuous drain current(Tc=25°C)	I _D	200	A
Pulsed drain current	I _{DM}	800	A
Power dissipation(Tc=25°C)	P _D	300	W
Single pulsed avalanche energy ¹⁾	E _{AS}	737	mJ
Thermal resistance, junction-case	R _{θJC}	0.42	°C/W
Operation and storage temperature	T _J , T _{STG}	-55 to 150	°C

Electrical characteristics

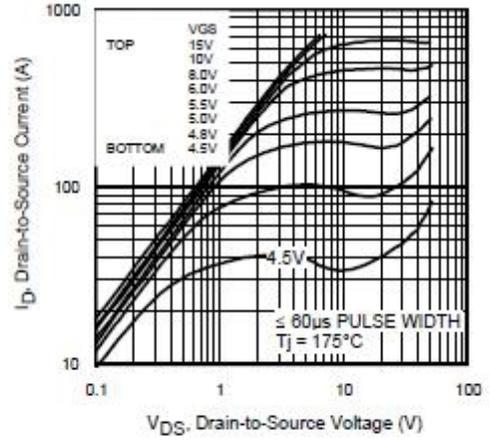
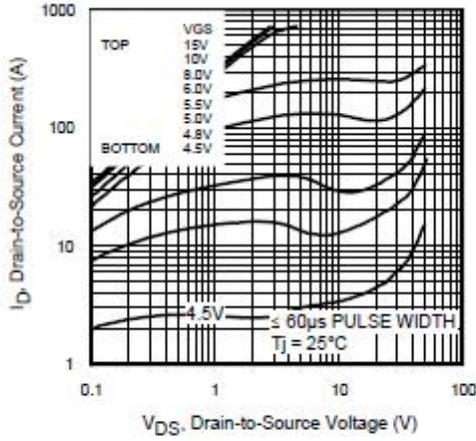
($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$BV_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	80			V
Drain Cut-Off Current	I_{DSS}	$V_{DS} = 68V, V_{GS} = 0V$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 0.1	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2	3	4	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 75A$		2.9	3.5	$m\Omega$
Dynamic Characteristics						
Input capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V, f=1MHz$		7610		pF
Output capacitance	C_{oss}			722		
Reverse transfer capacitance	C_{rss}			386		
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS}=60V, V_{GS}=10V, I_D = 75A$		183		pF
Gate-Source Charge	Q_{gs}			44		
Gate-Drain Charge	Q_{gd}			65		
Turn-on Delay Time	$T_{d(on)}$	$V_{GS}=10V, V_{DS}=48V, I_D = 75A, R_{GEN} = 2.6\Omega$		29		nS
Turn-on Rise Time	T_r			120		
Turn-Off Delay Time	$T_{d(off)}$			68		
Turn-Off Fall Time	t_f			74		
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	$I_S = 1A, V_{GS} = 0V$			1.2	V

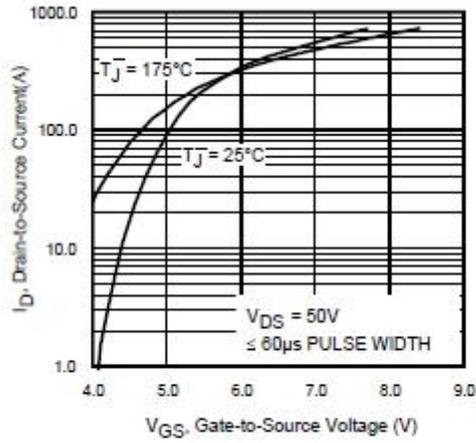
Note:

1. E AS is tested at starting $T_j = 25^\circ\text{C}$, $V_{DD} = 30V, V_{GS} = 10V, L = 0.3mH, R_g = 25 m\Omega$;

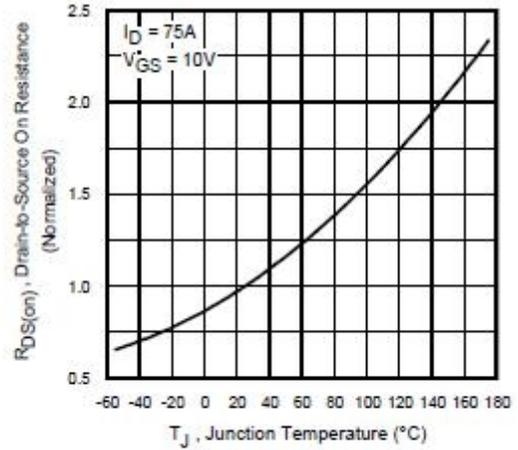
Typical Characteristics



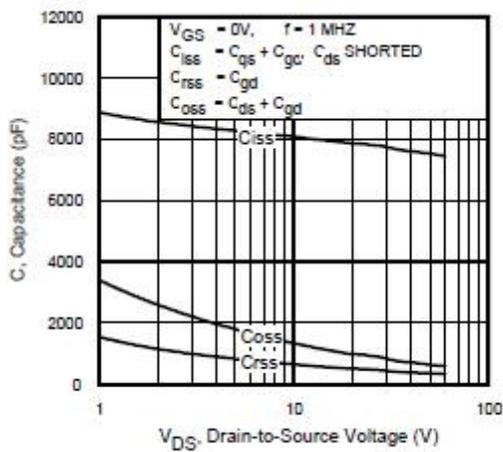
Typical Output Characteristics



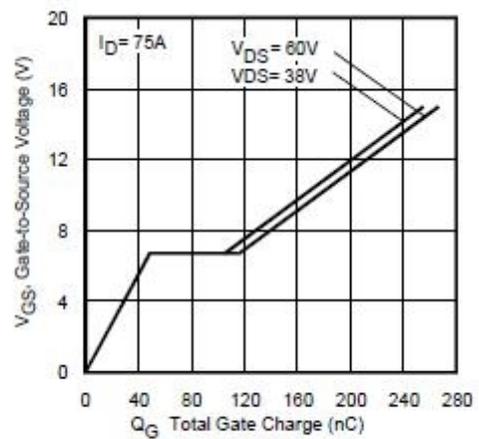
Typical Output Characteristics



Typical Transfer Characteristics

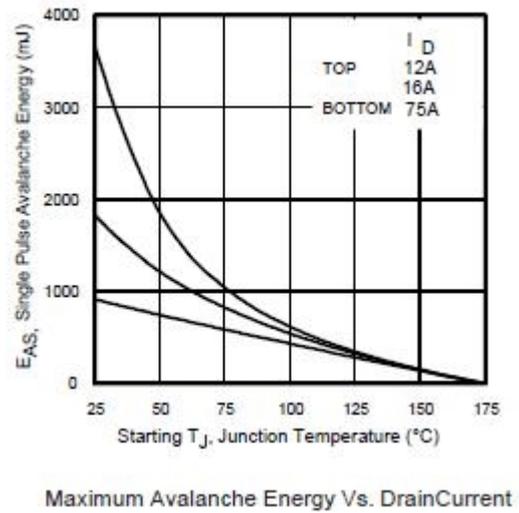
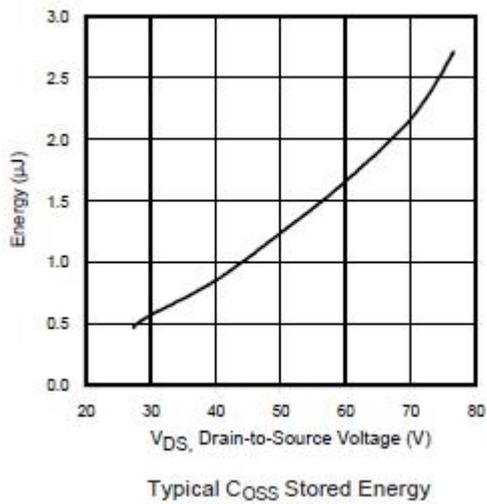
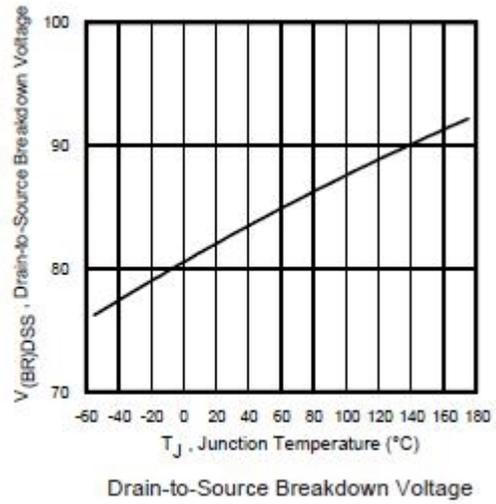
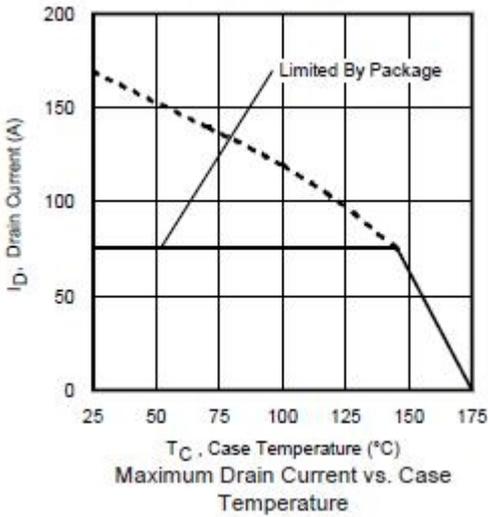
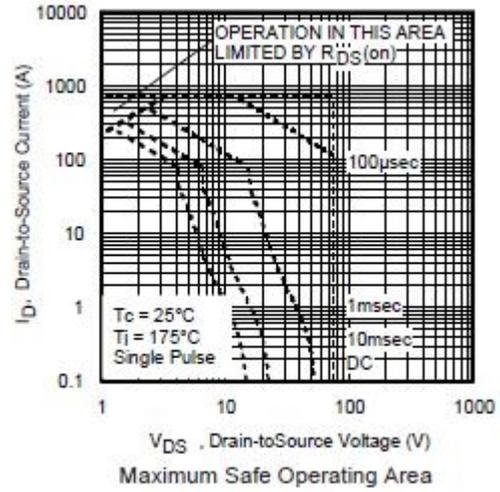
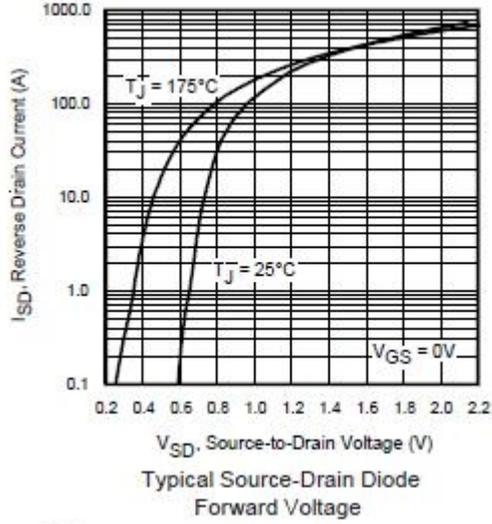


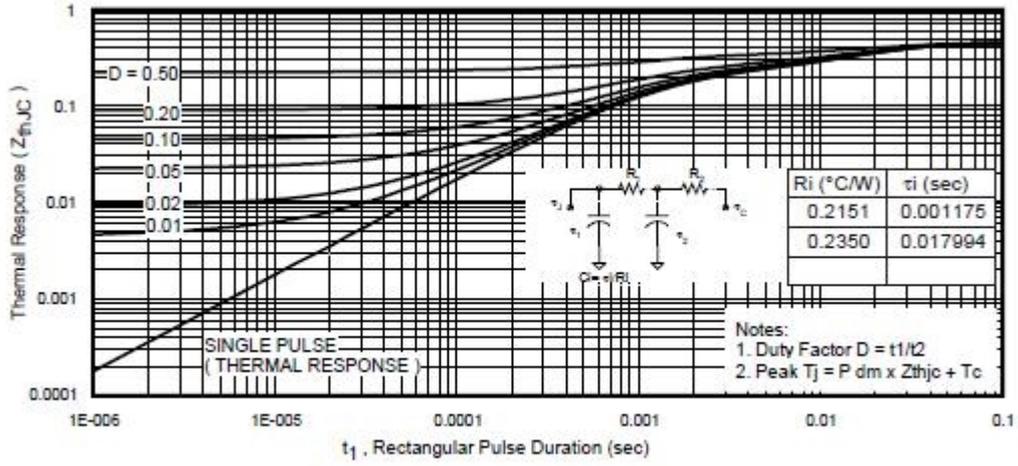
Normalized On-Resistance vs. Temperature



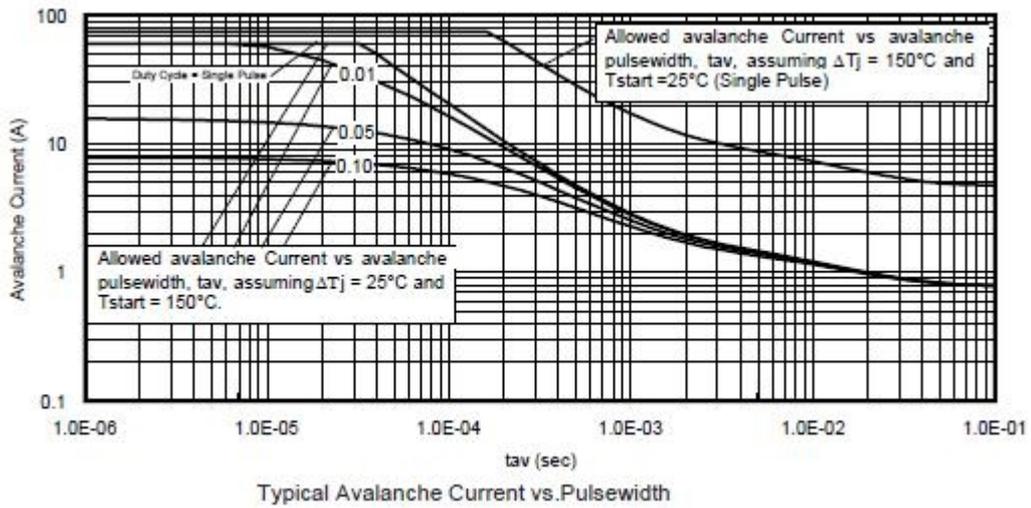
Typical Capacitance vs. Drain-to-Source Voltage

Typical Gate Charge vs. Gate-to-Source Voltage

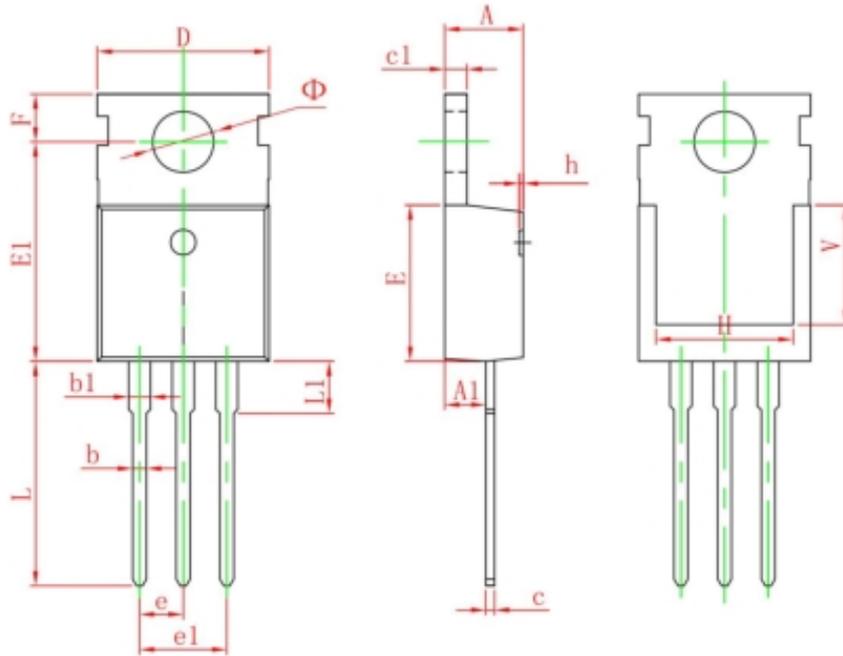




Maximum Effective Transient Thermal Impedance, Junction-to-Case



TO-220-3L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181
A1	2.250	2.550	0.089	0.100
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.330	0.650	0.013	0.026
c1	1.200	1.400	0.047	0.055
D	9.910	10.250	0.390	0.404
E	8.950	9.750	0.352	0.384
E1	12.650	13.050	0.498	0.514
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.900	13.400	0.508	0.528
L1	2.850	3.250	0.112	0.128
V	6.900 REF.		0.276 REF.	
Φ	3.400	3.800	0.134	0.150